



INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>	<i>Complete if Known</i>	
	Application Number	10/676,430
	Filing Date	September 30, 2003
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	Group Art Unit	2181
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Sheet 1 of 1	Attorney Docket No: 42P17035	

US PATENT DOCUMENTS					
Examiner Initial *	Cite No ¹	USP Document Number	Publication or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No ¹	Foreign Patent Document Country Code/Number/Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"PCT/US2004/043523 International Search Report", (6/15/2005), 3 pages	
		ABALI, BULENT , et al., "Hardware Compressed Main Memory: Operating System Support and Performance Evaluation", <u>IEEECS Log Number 114250</u> , (2001), 11 pages	
		ABALI, B. , et al., "Memory Expansion Technology (MXT): Software support and performance", <u>International Business Machines Corporation</u> , (2001), 15 pages	
		ABALI, BULENT , et al., "Performance of Hardware Compressed Main Memory", <u>IBM Research Report</u> , IBM T.J. Watson Research Center, Yorktown Heights, NY, (19 July 2000), 13 pages	
		ALAMELDEEN, ALAA R., et al., "Adaptive Cache Compression for High-Performance Processors", <u>Proceedings of the 31st Annual International Symposium on Computer Architecture (ISCA-31)</u> , Munich, Germany, (June 19-23, 2004), 12 pages	
		FRANASZEK, P. A., et al., "Algorithms and data structures for compressed-memory machines", <u>International Business Machines Corporation</u> , (2001), 14 pages	
		HALLNOR, ERIK G., et al., "A Compressed Memory Hierarchy using an Indirect Index Cache", <u>Advanced Computer Architecture Laboratory, EECS Department, University of Michigan, Ann Arbor, MI</u> , (2004), 17 pages	
		MASON, K. , "Cache System Having Ability to Store Compressed and Uncompressed Data Lines", <u>IBM, Research Disclosure Publications, Vol. 454, No. 150, XP007129887, ISSN: 0374-04353, (Feb. 2002),</u>	

EXAMINER

DATE CONSIDERED